

**BE Semester- V (ATKT CE) Question Bank  
Advanced Microprocessor**

**All questions carry equal marks (10 marks)**

Q.1	Draw and explain 8086 architecture.
Q.2	What does mean by Segmentation? Explain in brief.
Q.3	What are the different Flags in 8086 microprocessor?
Q.4	Explain Instruction Sets.
Q.5	What is an Interrupt? Explain different types of interrupt.
Q.6	Explain the format of Interrupt service routine.
Q.7	Write down the steps for Debugging of a Program.
Q.8	Write a programme to add two 8 bit numbers.
Q.9	Write a programme to subtract two 8 bit numbers.
Q.10	Write a programme to add two 16 bit numbers.
Q.11	Write a programme to subtract two 16 bit numbers.
Q.12	Write a programme to add 5 numbers of an array.
Q.13	Explain 8086 pin function.
Q.14	Minimum mode operations of 8086.
Q.15	Write short note on : Memory banks
Q.16	Write short note on: Multiplexing of Buses.
Q.17	Explain the Clock generation in 8086.
Q.18	Explain the 8288 bus controller.
Q.19	Draw and explain interfacing of 8284 with 8086.
Q.20	Draw and explain interfacing of 8288 with 8086.
Q.21	Explain 80286 architecture with diagram.
Q.22	Explain architecture of 80386 with diagram.
Q.23	Explain architecture of 80486 with diagram.
Q.24	Write down short note on: Architecture of Pentium Processor.
Q.25	Differentiate: Real mode and protected mode operations.
Q.26	Explain Virtual 8086 mode paging system.
Q.27	Write down the concept of RISC System.
Q.28	Differentiate RISC vs. CISC.
Q.29	Architecture of SUN SPARC with diagram.
Q.30	Maximum mode operations of 8086.
Q.31	What are the different busses in 8086? Explain in brief.
Q.32	Explain Instruction with an example: ADD, ADC, SUB, INC
Q.33	Explain Instruction with an example : PUSH, POP
Q.34	What is an addressing mode? List and explain addressing modes supported by the 8086.
Q.35	Give the coding format for immediate to register MOV instruction state example.
Q.36	Explain 8086 instructions/directive : LOOP, CMPS, EVEN
Q.37	What do you mean by executing and debugging a program? List the various debug commands with their meaning.
Q.38	Explain 1) MOV A, B. 2) MOV A, 8086 3) MOV A, [8086].
Q.39	Differentiate between MOV B, 1000 and MOV B,[1000].
Q.40	Explain 8086 internal architecture with the function of each component.

